

Electronic Acknowledgement Receipt

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| EFS ID: | 7722802 |
| Application Number: | 10801503 |
| International Application Number: | |
| Confirmation Number: | 1822 |
| Title of Invention: | Fractional-type Phase-Locked Loop circuit with compensation of phase errors |
| First Named Inventor/Applicant Name: | Guido Gabriele Albasini |
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| Filer: | Kevin Duane Jablonski/Cindy McKee |
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| Application Type: | Utility under 35 USC 111(a) |

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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes)/ Message Digest | Multi Part /.zip | Pages (if appl.) |
|-----------------|----------------------|---|--|------------------|------------------|
| 1 | | 2110-111-03_20100415_final_OAR_20100602.pdf | 198088 d6662ca3bc1bbdeca6a7682835ffb543a4990842 | yes | 17 |

| | Multipart Description/PDF files in .zip description | | | |
|--|---|-------|-----|--|
| | Document Description | Start | End | |
| | Amendment After Final | 1 | 1 | |
| | Claims | 2 | 10 | |
| | Applicant Arguments/Remarks Made in an Amendment | 11 | 17 | |

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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